Exam. Code : 105703
Subject Code : 1461

B.Sc. Information Technology 3rd Semester
SYSTEM ANALYSIS AND DESIGN
Paper—III

Time Allowed—3 Hours]   [Maximum Marks—75

Note :— (1) Attempt any *five* questions by selecting at least *one* question from each Section and the **fifth** question may be attempted from any section.

         (2) All questions carry equal marks.

SECTION—A

1. What is System Development Life Cycle (SDLC) ? What are various phases of SDLC ? Outline the phases of SDLC with a brief description of each.   15

2. Explain the following :
   (a) Cost-benefit Analysis   8
   (b) Role of System Analyst.   7

SECTION—B

3. (a) What do you understand by structured design ? Enumerate the steps involved in this process.

            8

   (b) What is Input/Output Design ? Illustrate the process involved in this design.   7

4. Explain the following :
   (a) Modular Design   8
   (b) Structured Design Tools.   7

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SECTION—C

5. What is system implementation ? What are the stages involved in this process ? How are these carried out ? Illustrate. 15

6. Explain the following :

   (a) Quality Assurance 8

   (b) Documentation Tools 7

SECTION—D

7. What is system testing ? Why is it needed ? What are different types of testing ? How are these carried out ? Illustrate. 15

8. What is the concept of system maintenance ? Why is it needed ? What are its different types ? How are these carried out ? Illustrate. 15